



PTO/SB/21 (09-04)

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Total Number of Pages in This Submission

6

Application Number	10/758,547
Filing Date	January 14, 2004
First Named Inventor	Anna Charny
Art Unit	2665
Examiner Name	Huy Duy Vu
Total Number of Pages in This Submission	6
Attorney Docket Number	CISCP844

ENCLOSURES (Check all that apply)

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Firm Name	Cindy S. Kaplan, Attorney at Law		
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Printed name	Cindy S. Kaplan		
Date	August 18, 2006	Reg. No.	40,043

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Cindy S. Kaplan	

Attorney Docket No. CISCP844
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Anna Charny et al.

Application No.: 10/758,547

Filed: January 14, 2004

For: PRIORITY PROPAGATION IN A
MULTI-LEVEL SCHEDULING
HIERARCHY

Examiner: Huy Duy Vu

Group: 2665

Date: August 18, 2006

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §§1.56 AND 1.97(c)

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Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO/SB/08A and PTO/SB/08B (copies of non-patent references attached) may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1652 (Order No. CISCP844).

Respectfully submitted,



Cindy S. Kaplan
Reg. No. 40,043

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/758,547
Filing Date	January 14, 2004
First Named Inventor	Anna Charny
Art Unit	2665
Examiner Name	Huy Duy Vu
Attorney Docket Number	CISCP844

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
A	US- 2005/0152374	07-14-2005	Cohen et al.		
B	US- 2006/0029079	02-09-2006	Cohen et al.		
C	US- 2006/0029080	02-09-2006	Kappler et al.		
D	US- 2006/0153243	07-13-2006	Shoham et al.		
E	US- 6,560,230	05-06-2003	Li et al.		
F	US- 6,691,312	02-10-2004	Sen et al.		
G	US- 6,836,475	12-28-2004	Chaskar et al.		
H	US- 6,412,000	06-25-2002	Riddle et al.		
	US-				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
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PTO/SB/08B (07-05)

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Sheet	1
of	1
Attorney Docket Number	CISCP844

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	I	Bennett et al., "Hierarchical Packet Fair Queuing Algorithms," IEEE/ACM Transactions on Networking (TON), Vol. 5, Issue 5, Oct. 1997, pp. 675-689.	
	J	Hou et al., "Service Disciplines for Guaranteed Performance Service,"Fourth Int'l Workshop on Real-Time Computing Systems and Applications,Oct. 27-29, 1997,pp. 244-250,Taipei.	
	K	Hagai et al., "Multiple Priority, Per Flow, Dual GCRA Rate Controller for ATM Switches," The 21st IEEE Conv., Apr. 11-12, 2000, pp. 479-482, Tel-Aviv, Israel.	
	L	Kim et al., "Three-level Traffic Shaper and its Application to Source Clock Frequency Recovery for VBR Services in ATM Networks," IEEE/ACM Vol.3, Iss.4, Aug. 1995, pp.450-458.	
	M	Dixit et al., "Traffic Descriptor Mapping and Traffic Control for Frame Relay Over ATM Netowrk," IEEE/ACM TON, Vol. 6, Issue 1, Feb. 1998, pp. 56-70.	
	N	Bennett et al., "WF2Q: Worst-case Fair Weighted Fair Queuing," INFOCOM '96 Fifteenth Annual Joint Conference of the IEEE, Mar. 24-28, 1996, Vol. 1, pp.120-128.	
	O	Bennett et al., "High-Speed, Scalable, and Accurate Implementation of Fair Queuing Algorithms in ATM Networks," ICNP '97,http://www-2.cs.cmu.edu/~hzhang/papers/ICNP97.pdf,1997.	

Examiner Signature	Date Considered
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